

Complete development system for creating Digital Camera solutions on the Philips Velocity RSP7+ platform.

# Digital Camera Kit for Velocity<sup>®</sup> RSP7+



**Athena Digital Camera Kit for Velocity**



## Description

The Athena Digital Camera Kit includes everything you need to develop, test, and debug embedded image processing systems on the Philips Velocity RSP7+ Rapid Silicon Prototyping platform. Kits are available for both monochrome and color applications and output standard JPEG images. Additional image processing functions can be implemented using Athena High Performance DSP accelerators, such as FFTs and FIR Filters.

The Athena Digital Camera Kit uses an optimized software plus hardware accelerator approach to implement JPEG compression. This maintains software flexibility while offloading the most computationally demanding portion of the JPEG compression to dedicated DSP hardware. The end result is a higher performance and lower power solution, without the design and implementation costs of a dedicated hardware-only design.

## Benefits

- Ideal for developing both still camera and video camera systems for cellular phones, security, machine vision, automotive, and 2D scanning
- Rapid silicon prototyping in the Velocity environment
- Real-time HW/SW co-development and co-verification

## Documentation

Detailed documentation is available with this product.

## Features

- Athena High Performance JPEG Hardware Accelerator for ultra-fast, low power imaging
- Optimized software and hardware reference design for image capture and compression
- Athena A7 prototyping expansion boards using Altera® APEX™ 20KE600K FPGAs
- Kodak KAC0310 EB digital camera

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## Digital Camera Kit Includes

- Athena High Performance JPEG Hardware Accelerator
- Independent JPEG Group software optimized to exploit the Athena High Performance JPEG Hardware Accelerator
- Athena A7-600 Prototyping Expansion Boards (2)
- Kodak KAC0310 EB Digital Camera
- Camera interface cable
- Programming cable and ATX power supply upgrade for Velocity
- Documentation

## Other Information

Development Platform	Philips Velocity RSP7+
Host PC OS	Windows 95/98/NT/2000
Host PC Ports	One parallel port for use with ByteBlasterMV programming cable
FPGA Programming S/W	Altera ASAP2 (minimum), Altera Quartus <sup>™</sup> (recommended)

## Contact Information

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